
#### Abstract

General Description The MAX4995A/MAX4995AF/MAX4995AL/MAX4995B/ MAX4995C programmable current-limit switches feature internal current limiting to prevent damage to host devices due to faulty load conditions. These analog switches feature a low $130 \mathrm{~m} \Omega$ (typ) on-resistance and operate from $\mathrm{a}+1.7 \mathrm{~V}$ to +5.5 V input voltage range. The current limit is adjustable from 50 mA to 600 mA , making these devices ideal for SDIO (secure digital input/output) and other load-switching applications. Each device in the family handles an overcurrent event differently depending on the option selected. The MAX4995A/MAX4995AF/MAX4995AL go into an autoretry mode, the MAX4995B latches off the switch, and the MAX4995C places the device in a continuous current-limit mode. Additional safety features include thermal shutdown to prevent overheating and reverse-current blocking to prevent current from being driven back into the source. The MAX4995A/MAX4995AF/MAX4995AL/MAX4995B/ MAX4995C are available in a tiny 10-pin, $1.4 \mathrm{~mm} \times 1.8 \mathrm{~mm}$ UTQFN package and operate over the $-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ extended temperature range.


## Applications

SDIO Ports
USB Ports
Notebook VGA Ports
GPS
Cell Phones
MP3 Players
UTCA/ATCA Platforms

Typical Operating Circuit appears at end of data sheet.

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Ordering Information/Selector Guide

| PART | PIN- <br> PACKAGE | ON POLARITY | OVERCURRENT <br> RESPONSE | SHORT-CIRCUIT <br> RESPONSE | TOP MARK |
| :--- | :--- | :---: | :---: | :---: | :---: |
| MAX4995AAUT+T* | 6 SOT23 | Actlve-High | Autoretry | Normal | + ACNZ |
| MAX4995AAVB+T | 10 UTQFN | Active-High | Autoretry | Normal | + AAM |
| MAX4995AFAUT+T* | 6 SOT23 | Active-High | Autoretry | Fast | + ACOE |
| MAX4995AFAVB+T | 10 UTQFN | Active-High | Autoretry | Fast | + AAR |
| MAX4995ALAUT+T* | 6 SOT23 | Active-Low | Autoretry | Normal | + ACDA |

All devices operate over the $-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ temperature range.
+Denotes a lead(Pb)-free/RoHs-compliant package. $T=$ Tape and reel.
*Future product. Contact factory for availability.
Ordering Information/Selector Guide continued at end of data sheet.

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

## 50mA to 600mA Programmable Current-Limit Switches

## ABSOLUTE MAXIMUM RATINGS

| IN, ON, $\overline{O N}, \overline{\text { FLAG, OUT, and SETI to GND ............-0.3V to +6V }}$ |  |
| :---: | :---: |
| Current into Any Pin (Except IN, OUT). | 20 mA |
| OUT Short Circuit to GND ........................................... 800 mA |  |
| Continuous Power Dissipation ( $\left.\mathrm{T}_{\mathrm{A}}=+70^{\circ} \mathrm{C}\right)($ Note 1) |  |
| 10-Pin UTQFN (derate $6.99 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above |  |
| $\left.\mathrm{T}_{\mathrm{A}}=+70^{\circ} \mathrm{C}\right)$ | .559mW |

Junction-to-Ambient Thermal Resistance ( $\mathrm{\theta JA}^{\text {) }}$ (Note 2)
$143.1^{\circ} \mathrm{C} / \mathrm{W}$
Operating Temperature Range .......................... $40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$
Storage Temperature Range ............................. $65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$
Junction Temperature ..................................................... $+150^{\circ} \mathrm{C}$
Lead Temperature (soldering, 10s) ................................. $+300^{\circ} \mathrm{C}$

Note 1: These power limits are defined by the thermal characteristics of the package, maximum function temperature ( $+150^{\circ} \mathrm{C}$ ), and the JEDEC51-7 defined setup. Maximum power dissipation could be lower, limited by the thermal-shutdown protection included in this IC.
Note 2: Package thermal resistances were obtained using the method described in JEDEC specification JESD51-7, using a fourlayer board. For detailed information on package thermal considerations, refer to www.maxim-ic.com/thermal-tutorial.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## ELECTRICAL CHARACTERISTICS

$\left(\mathrm{V}_{\mathrm{IN}}=+1.7 \mathrm{~V}\right.$ to $+5.5 \mathrm{~V}, \operatorname{RSETI}=94.3 \mathrm{k} \Omega, \mathrm{CIN}^{2}=1 \mu \mathrm{~F}$, and $\mathrm{T}_{\mathrm{A}}=\mathrm{T}_{\mathrm{J}}=-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$, unless otherwise noted. Typical values are at $\mathrm{V}_{\mathrm{IN}}=+3.3 \mathrm{~V}$, $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$.)

| PARAMETER | SYMBOL | CONDITIONS | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| SUPPLY OPERATION |  |  |  |  |  |  |
| Operating Voltage | VIN |  | 1.7 |  | 5.5 | V |
| Quiescent Current | IQ | IOUT $=0$, switch on, $\mathrm{V}_{\mathrm{IN}}=3.3 \mathrm{~V}$ |  | 170 | 300 | $\mu \mathrm{A}$ |
| Latchoff Current | ILATCH | $\mathrm{V}_{\mathrm{IN}}=3.3 \mathrm{~V}$, IOUT $=0$ after an overcurrent fault (MAX4995B) |  | 8 | 15 | $\mu \mathrm{A}$ |
| Shutdown Forward Current | ISHDN | $\mathrm{V}_{\text {ON }}=0, \mathrm{~V}_{\text {ON }}=\mathrm{V}_{\text {IN }}, \mathrm{V}_{\text {IN }}=5.5 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=0$ |  | 0.01 | 5 | $\mu \mathrm{A}$ |
| Shutdown Reverse Current | IRSHDN | $\begin{aligned} & \mathrm{V}_{\mathrm{ON}}=0, \mathrm{~V} \mathrm{ON}=\mathrm{V}_{\mathrm{IN}}, \mathrm{~V}_{\mathrm{IN}}=1.7 \mathrm{~V}, \\ & \mathrm{~V}_{\mathrm{OUT}}=5.5 \mathrm{~V} \text { (current into OUT) } \end{aligned}$ |  | 0.01 | 1 | $\mu \mathrm{A}$ |
| INTERNAL FET |  |  |  |  |  |  |
| Switch-On Resistance | Ron | $\mathrm{V}_{\text {IN }}=3.3 \mathrm{~V}$, IOUT lower than ILIM |  | 130 | 350 | $\mathrm{m} \Omega$ |
| Normalized Current-Limit Accuracy |  | $\begin{aligned} & \text { ILIM }=50 \mathrm{~mA} \text { to } 600 \mathrm{~mA}, \mathrm{~V} \text { IN }- \text { V } \mathrm{V} \text { IT }=1 \mathrm{~V}, \\ & \mathrm{~V}_{\text {IN }}=3.3 \mathrm{~V}(\text { Note } 3) \end{aligned}$ | 0.9 | 1 | 1.1 | - |
| (RSETI + 2.48) x ILIM Product |  | $\begin{aligned} & \text { ILIM }=50 \mathrm{~mA} \text { to } 600 \mathrm{~mA}, \mathrm{~V} \text { IN }- \text { VOUT }=1 \mathrm{~V}, \\ & \mathrm{~V}_{\text {IN }}=3.3 \mathrm{~V} \end{aligned}$ | 26138 | 29042 | 31946 | V |
| Reverse Blocking Current |  | VOUT $>$ VIN +300 mV after reverse-currentlimit shutdown |  |  | 10 | $\mu \mathrm{A}$ |
| Reverse Blocking Threshold |  | $V_{\text {OUT }}=V_{\text {IN }}+300 \mathrm{mV}$, OUT falling until switch turns on | 35 | 110 | 210 | mV |
| FLAG Assertion Drop Voltage Threshold | $V_{\text {FA }}$ | Increase (VIN - VOUT) drop until FLAG asserts, Iout limiting, $\mathrm{V}_{\mathrm{IN}}=3.3 \mathrm{~V}$ |  | 650 |  | mV |
| ON, $\overline{O N}$ INPUT |  |  |  |  |  |  |
| ON, $\overline{O N}$ Input Leakage | ILEAK | $\mathrm{V}_{\text {ON, }} \mathrm{V}_{\text {ON }}=\mathrm{V}_{\text {IN }}$ or GND | -1 |  | +1 | $\mu \mathrm{A}$ |
| ON, $\overline{\text { ON }}$ Input Logic-High Voltage | $\mathrm{V}_{\mathrm{IH}}$ |  | 1.6 |  |  | V |
| ON, $\overline{O N}$ Input Logic-Low Voltage | $\mathrm{V}_{\text {IL }}$ |  |  |  | 0.4 | V |
| FLAG OUTPUT |  |  |  |  |  |  |
| FLAG Output Logic-Low Voltage |  | $\mathrm{ISINK}=1 \mathrm{~mA}$ |  |  | 0.4 | V |
| $\overline{\text { FLAG }}$ Output Leakage Current |  | $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\mathrm{FLAG}}=5.5 \mathrm{~V}$, $\overline{\mathrm{FLAG}}$ deasserted |  |  | 1 | $\mu \mathrm{A}$ |

## 50mA to 600mA Programmable Current-Limit Switches

## ELECTRICAL CHARACTERISTICS (continued)

$\left(V_{I N}=+1.7 \mathrm{~V}\right.$ to $+5.5 \mathrm{~V}, \mathrm{RSETI}^{2}=94.3 \mathrm{k} \Omega, \mathrm{C}_{\mathrm{IN}}=1 \mu \mathrm{~F}$, and $\mathrm{T}_{\mathrm{A}}=\mathrm{T}_{J}=-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$, unless otherwise noted. Typical values are at $\mathrm{V}_{\mathbb{I}}=+3.3 \mathrm{~V}$, $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$.)

| PARAMETER | SYMBOL | CONDITIONS |  | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| DYNAMIC |  |  |  |  |  |  |  |
| Turn-On Time | tSS | $\mathrm{V}_{\mathrm{IN}}=3.3 \mathrm{~V}$, COUT $=1 \mu \mathrm{~F}, \mathrm{R}_{\mathrm{L}}=20 \Omega$, Figure 1, (Note 4) |  | 120 |  |  | $\mu \mathrm{S}$ |
| Turn-Off Time | toff | Switch from on to off, V IN $=3.3 \mathrm{~V}, \mathrm{COUT}=1 \mu \mathrm{~F}$, $R L=20 \Omega$, Figure 1 (Note 4) |  | 120 |  |  | $\mu \mathrm{s}$ |
|  | tLIM | V IN $=3.3 \mathrm{~V}$, RSETI $=$ $578 \mathrm{k} \Omega$, output high and then short-circuit applied | MAX4995A/AL/B/C |  | 5 |  | $\mu \mathrm{s}$ |
|  |  |  | MAX4995AF |  | 1.5 |  |  |
| Blanking Time | tBLANK | (Note 5) |  | 10 | 16.3 | 22.6 | ms |
| Retry Time | tretry | MAX4995A/MAX4995AF/MAX4995AL (Note 5) |  | 320 |  | 723.2 | ms |
| THERMAL PROTECTION |  |  |  |  |  |  |  |
| Thermal Shutdown |  |  |  |  | +150 |  | ${ }^{\circ} \mathrm{C}$ |
| Thermal-Shutdown Hysteresis |  |  |  |  | 15 |  | ${ }^{\circ} \mathrm{C}$ |

Note 3: ILIM is forward current limit.

$$
\mathrm{L}_{\mathrm{LIM}}(\mathrm{~mA})=\frac{29042(\mathrm{~V})}{\mathrm{R}_{\mathrm{SETI}}(\mathrm{k} \Omega)+2.48(\mathrm{k} \Omega)}
$$

Note 4: Turn-on time and turn-off time are defined as the difference in the time between when the output crosses 10\% and 90\% of the final output voltage.
Note 5: Blanking time and retry time are generated by the same oscillator. Therefore, the ratio of
${ }^{t_{\text {RETRY }}}$
tBLANK
is a constant value of 32 . See Figures 2 and 3.


Figure 1. Timing Diagram for Measuring Turn-On Time (tss) and Turn-Off Time (toff).

## 50mA to 600mA Programmable Current-Limit Switches

$\left(\mathrm{V}_{\text {IN }}=+3.3 \mathrm{~V}, \mathrm{C}_{\text {IN }}=1 \mu \mathrm{~F}, \mathrm{C}_{\text {OUT }}=1 \mu \mathrm{~F}, \mathrm{R}_{\text {SETI }}=94.2 \mathrm{k} \Omega, \mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}\right.$, unless otherwise noted. $)$



CURRENT-LIMIT RESPONSE (MAX4995AF)


CURRENT-LIMIT RESPONSE



## 50mA to 600mA Programmable Current-Limit Switches

Typical Operating Characteristics (continued)
$\left(V_{I N}=+3.3 \mathrm{~V}, \mathrm{C}_{\text {IN }}=1 \mu \mathrm{~F}, \mathrm{COUT}=1 \mu \mathrm{~F}, \mathrm{R}_{\text {SETI }}=94.2 \mathrm{k} \Omega, \mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}\right.$, unless otherwise noted. $)$


## 50mA to 600mA Programmable Current-Limit Switches

# Typical Operating Characteristics (continued) 

$\left(\mathrm{V}_{\text {IN }}=+3.3 \mathrm{~V}, \mathrm{C}_{\text {IN }}=1 \mu \mathrm{~F}\right.$, COUT $=1 \mu \mathrm{~F}$, RSETI $=94.2 \mathrm{k} \Omega, \mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise noted. $)$

$\overline{\text { FLAG RESPONSE (OVERLOAD) CONDITION }}$


SWITCH DROPOUT VOLTAGE
vs. TEMPERATURE



# 50mA to 600 mA Programmable Current-Limit Switches 

| PIN (UTQFN) |  | NAME | FUNCTION |
| :---: | :---: | :---: | :---: |
| MAX4995AL | MAX4995_ |  |  |
| 1, 10 | 1, 10 | IN | Power Input. Bypass IN with a $1 \mu \mathrm{~F}$ ceramic capacitor to ground. Use higher capacitance to prevent large load transients from pulling down the supply voltage if necessary. Connect both power inputs (IN) together. |
| 2 | 2 | $\overline{F L A G}$ | Open-Drain, Overload Indicator Output. FLAG goes low when the overload fault duration exceeds the blanking time, reverse current is detected, thermal shutdown mode is active, or SETI is connected to ground. |
| 3 | - | $\overline{\mathrm{ON}}$ | Active-Low, Switch-On Input. Drive $\overline{\mathrm{ON}}$ low to turn on the switch. |
| - | 3 | ON | Active-High, Switch-On Input. Drive ON high to turn on the switch. |
| 4 | 4 | GND | Ground |
| 5 | 5 | SETI | Overload Current Limit Adjust. Connect a resistor from SETI to ground to program the overcurrent limit. Do not connect any capacitance larger than 20pF to SETI. |
| 6, 9 | 6, 9 | N.C. | No Connect. Not internally connected. |
| 7, 8 | 7, 8 | OUT | Switch Output. Bypass OUT with a $1 \mu \mathrm{~F}$ capacitor to ground. Connect both outputs (OUT) together. |

## Detailed Description

The MAX4995A/MAX4995AF/MAX4995AL/MAX4995B/ MAX4995C programmable current-limit switches operate from +1.7 V to +5.5 V and provide internal current limiting adjustable from 50 mA to 600 mA . These devices feature a fixed blanking time and a FLAG output that notifies the processor when a fault condition is present.

## Programmable Current Limit

A resistor from SETI to GND programs the current limit for the switch (see the Setting the Current Limit section). If the output current exceeds the current limit for a time equal to or longer than tBLANK, the output flag asserts and the MAX4995A/MAX4995AF/MAX4995AL enter the autoretry mode. The MAX4995B latches off the switch, and the MAX4995C enters the continuous current-limit mode.

## Autoretry (MAX4995A/MAX4995AF/ MAX4995AL)

When the forward current reaches the current-limit threshold, the tBLANK timer begins counting (Figure 2). FLAG asserts if the overcurrent-limit condition is present for tBLANK. The timer resets if the overcurrent condition disappears before the blanking time (tBLANK) has elapsed. A retry time delay (tRETRY) starts immediately after the blanking time has elapsed and during that time, the switch latches off. At the end of tretry, the
switch turns on again. If the fault still exists, the cycle repeats. If the fault has been removed, the switch stays on. During this cycle, FLAG stays low. In autoretry if the thermal power rating of the package is exceeded, the MAX4995A/MAX4995AF/MAX4995AL go into thermal shutdown.
The autoretry feature saves system power in case of an overcurrent or short-circuit condition. During tBLANK time when the switch is on, the supply current is held at the current limit. During time tRETRY when the switch is off, the current through the switch is zero. Thus, the average output current is much less than the programmed current limit. Calculate the average output current using the following equation:

$$
\text { LLOAD }=\text { ILIM [tBLANK/(tBLANK }+ \text { tRETRY) }]
$$

With a typical tBLANK $=16.3 \mathrm{~ms}$ and typical tRETRY $=$ 524 ms , the duty cycle is $3 \%$, resulting in a $97 \%$ power savings over the switch being on the entire time.

## Latchoff (MAX4995B)

When the forward current reaches the current-limit threshold, the tBLANK timer begins counting (Figure 3). FLAG asserts if an overcurrent-limit condition is present for greater than tblank time. The timer resets if the overcurrent condition disappears before tBLANK has elapsed. The switch turns off if the overcurrent condition

## 50mA to 600mA Programmable Current-Limit Switches


continues beyond the blanking time. Reset the switch by either toggling the control logic (ON) or cycling the input voltage. If the thermal power rating of the package is exceeded during tblank, the MAX4995B goes into thermal shutdown.

Continuous Current Limit (MAX4995C)
When the forward current reaches the forward currentlimit threshold, the MAX4995C limits the output current to the programmed current limit. FLAG asserts if the current limit is present for tBLANK and deasserts when the overload condition is removed. In this mode, if the thermal power rating of the package is exceeded, the MAX4995C goes into thermal shutdown.

## Switch-On/Off Control

The ON input for the MAX4995_/MAX4995AF and ON input for the MAX4995AL control the switch; see Table 1. Toggle ON for the MAX4995B to reset the fault condition once the short current is detected and the device shuts down.

Table 1. Switch Truth Table

| MAX4995_/ <br> MAX4995AF | MAX4995AL | SWITCH <br> STATUS |
| :---: | :---: | :---: |
| ON | $\overline{\mathbf{O N}}$ |  |
| 0 | 1 | Off |
| 1 | 0 | On |

## Reverse-Current Protection

The MAX4995 features a reverse-current protection circuit that limits the backflow current to $10 \mu \mathrm{~A}$ when the output voltage exceeds the input voltage by 110 mV (typ). The switch turns off and FLAG asserts without waiting for tblank to elapse. The switch turns back on and FLAG deasserts when the output voltage drops below the detecting threshold by 10 mV (typ).

## 50mA to 600mA Programmable <br> Current-Limit Switches



Figure 2. Autoretry Fault Diagram


Figure 3. Latchoff Fault Diagram

FLAG Indicator
$\overline{F L A G}$ is an open-drain fault indicator output and requires an external pullup resistor to a DC supply. $\overline{\text { FLAG }}$ goes low when any of the following conditions occurs:

- The device is in current-limit mode.
- The OUT voltage is above the IN voltage by more than 110 mV (typ).
- The die temperature exceeds the thermal-shutdown temperature limit of $+150^{\circ} \mathrm{C}$.
- SETI is connected to ground.

Thermal Shutdown
Thermal-shutdown circuitry protects the devices from overheating. The switch turns off and FLAG goes low immediately when the junction temperature exceeds $+150^{\circ} \mathrm{C}$ (typ). The switch turns on again after the device temperature drops by approximately $15^{\circ} \mathrm{C}$ (typ).

# 50mA to 600mA Programmable Current-Limit Switches 

## Applications Information

## Setting the Current Limit

A resistor from SETI to ground programs the current-limit value for the MAX4995. Table 2 lists various current limits set by different resistor values at SETI. Shorting SETI to ground asserts FLAG.
Use the following formula to calculate the current limit:

$$
\mathrm{R}_{\text {SETI }}(\mathrm{k} \Omega)=\frac{29042(\mathrm{~V})}{\lim (\mathrm{mA})}-2.48(\mathrm{k} \Omega)
$$

Using an RSETI with a value smaller than $45.8 \mathrm{k} \Omega$ results in a higher current limit. A programmed output current greater than 660mA can damage the device.
Connecting any capacitance larger than 20 pF to SET। can cause instability.

Table 2. Current Limit vs. Resistor Values

| RSETI (k $\boldsymbol{\Omega}$ ) | TYPICAL CURRENT LIMIT (mA) |
| :---: | :---: |
| 45.8 | 602 |
| 55.6 | 500 |
| 70.6 | 397 |
| 94.2 | 300 |
| 143 | 200 |
| 191 | 150 |
| 287 | 100 |
| 576 | 50 |
| $\infty$ (Open) | 0 |

## Input Capacitor

Connect a capacitor from IN to GND to limit the input voltage drop during momentary output short-circuit conditions. Use a $1 \mu \mathrm{~F}$ minimum ceramic capacitor for proper device operation. Larger capacitor values reduce the voltage undershoot at the input.
Due to the very fast current-limit reaction time of the MAX4995AF, a larger input capacitance might need to be connected at the input to dampen oscillation due to long wires. Choose a value large enough to ensure IN doesn't exceed the absolute maximum ratings.

## Output Capacitor

For stable operation over the full temperature range and over the full programmable current-limit range, use a $1 \mu \mathrm{~F}$ ceramic capacitor from OUT to ground.
If the load capacitance is too large, then current may not have enough time to charge the capacitance and the device assumes that there is a faulty load condition. Calculate the maximum capacitive load (CMAX) value that can be connected to OUT using the following formula:

$$
\mathrm{C}_{\mathrm{MAX}}(\mu \mathrm{~F})=\frac{\mathrm{I}_{\mathrm{LIM}}(\mathrm{~mA}) \times \mathrm{t}_{\mathrm{BLANK}(\mathrm{MIN})}(\mathrm{ms})}{\mathrm{V}_{\mathrm{IN}}(\mathrm{~V})}
$$

For example, for $\operatorname{VIN}=3.3 \mathrm{~V}$, tBLANK $(\mathrm{MIN})=10 \mathrm{~ms}$, and ILIM $=300 \mathrm{~mA}$, CMAX equals $909 \mu \mathrm{~F}$.
Due to the very fast current-limit reaction time of the MAX4995AF, a larger output capacitance might need to be connected at the output to dampen oscillation due to long wires. Choose a value large enough to ensure OUT doesn't exceed the absolute maximum ratings.

## Layout and Thermal Dissipation

To optimize the switch response time to output shortcircuit conditions, it is very important to keep all traces as short as possible to reduce the effect of undesirable parasitic inductance. Place input and output capacitors as close as possible to the device. IN and OUT must be connected with wide, short traces to the power bus. During normal operation, the power dissipation is small and the package temperature change is minimal. If the output is continuously shorted to ground at the maximum supply voltage, the operation of the switches with the autoretry option does not cause problems because the total power dissipated during the short is scaled by the duty cycle:

$$
\mathrm{P}_{\mathrm{MAX}}=\frac{\mathrm{V}_{\mathrm{IN}(\mathrm{MAX})} \times \mathrm{I}_{\mathrm{OUT}(\mathrm{MAX})} \times \mathrm{t}_{\mathrm{BLANK}}}{\mathrm{t}_{\text {RETRY }}+\mathrm{t}_{\text {BLANK }}}
$$

Attention must be given to the MAX4995C continuous current-limit version when the power dissipation during a fault condition may cause the device to reach thermal shutdown threshold.

## 50mA to 600mA Programmable Current-Limit Switches

Ordering Information/Selector Guide (continued)

| PART | PIN- <br> PACKAGE | ON POLARITY | OVERCURRENT <br> RESPONSE | SHORT-CIRCUIT <br> RESPONSE | TOP MARK |
| :--- | :--- | :---: | :---: | :---: | :---: |
| MAX4995ALAVB+T | $10-U T Q F N$ | Active-Low | Autoretry | Normal | +AAN |
| MAX4995BAUT+T* | 6 SOT23 | Active-High | Latchoff | Normal | + ACDB |
| MAX4995BAVB+T | $10-U T Q F N ~$ | Active-High | Latchoff | Normal | + AAO |
| MAX4995CAUT+T* | 6 SOT23 | Active-High | Continuous | Normal | + ACOD |
| MAX4995CAVB+T | $10-U T Q F N ~$ | Active-High | Continuous | Normal | + AAQ |

All devices operate over the $-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ temperature range.
+Denotes a lead(Pb)-free/RoHs-compliant package. $T$ = Tape and reel.
*Future product. Contact factory for availability.

Pin Configurations (continued)


## 50mA to 600mA Programmable Current-Limit Switches



Chip Information
PROCESS: BiCMOS
Package Information
For the latest package outline information and land patterns, go to www.maxim-ic.com/packages.

| PACKAGE TYPE | PACKAGE CODE | DOCUMENT NO. |
| :---: | :---: | :---: |
| 10 UTQFN | V101A1CN+1 | $\underline{\mathbf{2 1 - 0 0 2 8}}$ |
| 6 SOT 23 | $\cup 6 S N+1$ | $\underline{\mathbf{2 1 - 0 0 5 8}}$ |

## 50mA to 600mA Programmable Current-Limit Switches

| REVISION NUMBER | REVISION DATE | DESCRIPTION | PAGES CHANGED |
| :---: | :---: | :---: | :---: |
| 0 | 11/08 | Initial release | - |
| 1 | 6/09 | Corrected Ordering Information. | 1,11 |

